

# 8.9Gb/s real-time quantum random numbers with verified security

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Quantum random number generators (QRNG) rely on the laws of quantum physics to produce genuine randomness. Access to proper random numbers is paramount for many applications, including both classical and quantum cryptographic protocols.

Here we present an implementation of a vacuum fluctuation based QRNG [1], where the vacuum noise is measured with a shot noise limited homodyne detector (see Fig. 1). The detector output was digitized and randomness extraction was performed in real-time. We achieved a speed of 8.9 Gbit/s, which is the fastest real-time implementation we are aware of to this date. A new fully quantum secure proof for the QRNG model is provided which takes into account finite-bandwidth effects of a realistic device. A metrological grade characterization of the detector completes the security verification.

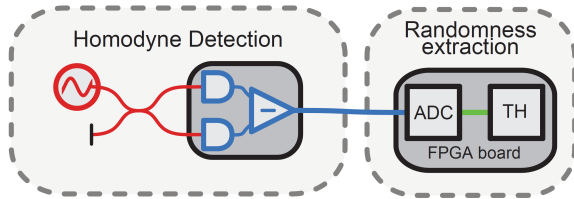


Figure 1: Vacuum fluctuation QRNG scheme. A homodyne measurement on the vacuum state is performed. The recorded noise spectrum is digitized. TH denotes Toeplitz hashing extraction performed in FPGA.

The security of random generators was, so far, mostly verified by running statistical tests on the output. While this approach can detect malfunctioning, it does not improve the security compared to classical RNG. The improved security of a device-dependent QRNG implementation is based on a precise characterization of the devices [2], which in turn needs to be in line with a proven quantum-mechanical QRNG model to verify the unpredictability of the measured outcomes with respect to the probabilistic interpretations of quantum mechanics [3].

Here, the two approaches, quantum proof security analysis and metrological grade characterization, are combined to achieve security with respect to quantum side information. We model the homodyne detector as a linear time-independent (LTI) system, described by a transfer function and an independent additive noise spectrum. A quantum secure proof of the given model is provided with regard of the added noise and the introduced correlations. We independently characterized the effective LTI transfer function of the detector system in a classical regime as well as the electronic-noise spectrum. From the model, we calculated a min-entropy of 0.6 secure bits per measured bit and the security parameter was determined from the difference be-

tween the predicted shot-noise spectrum, based on the detector characterization, and the actual measured shot-noise spectrum.

Efficient and secure classical postprocessing provides the last step for making QRNG technology practical for a wide range of applications. Randomness extraction must not compromise previously analyzed and established security. Toeplitz hashing is a proven method for randomness extraction in both hardware and software implementations [4]. Toeplitz matrices are a universal family of hash functions. Therefore, according to the Leftover Hash Lemma, they can be used as a strong randomness extractor.

We implemented a Toeplitz extractor in a high performance Xilinx Kintex UltraScale FPGA chip. The homodyne detector output is sampled with a 16 bit ADC at 1GS/s and fed to the FPGA. All the sampled data is processed without discarding any bits. This was achieved using the inherent parallel processing feature of the chip and the right tradeoff of the clock frequency and the number of implemented logic elements. Matrix multiplication was done using the submatrix method where the submatrices are generated on every clock cycle [5]. Submatrices are small enough to fit efficiently into FPGA logic, while the full matrix is big enough to provide high extraction efficiency. Timing constraints within the chip are met using pipelining and area constraining. Matrix dimensions are chosen according to leftover hash lemma for particular min-entropy and predefined security parameter. In this experiment we were able to accomplish 8.9Gb/s true random numbers output.

High-rate QRNG implementation are of great interest to commercial applications. A metrological grade approach, together with a quantum secure proof, gives a clear advantage over classical security verification schemes based on statistical tests.

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